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Substitute for form 1449A/PTO		Complete if Known	
INFORMATION DISCLOSURE STATEMENT BY APPLICANT		Application Number	TBD
		Filing Date	June 25, 2004
		First Named Inventor	HIDEHIRO NAKAMURA
		Art Unit	TBD 2841
		Examiner Name	TBD I. Patel
Sheet 1 of 1	Attorney Docket Number	1204.43988X00	

U.S. PATENT DOCUMENTS					
Examiner Initials*	Cite No. ¹	Document Number Number-Kind Code ² (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		US-			
		US-			
		US-			

FOREIGN PATENT DOCUMENTS						
Examiner Initials*	Cite No. ¹	Foreign Patent Document Country Code ² -Number ³ - Kind Code ⁴ (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	Abstract/ Translation
IBP		JP- 2001-326458	11/22/2001	Matsushita Electric Ind. Co		X
		JP-11-251703	9/17/1999	Matsushita Electric Ind. Co		X
		JP-2001-326459	11/22/2001	North:KK		X
		JP-2001-015920	1/19/2001	Toshiba Corp.		X
		JP-06-350258	12/22/1994	Toshiba Corp.		X
		JP-2001-036250	2/9/2001	Mitsubishi Electric Corp.		X
		JP-2000-101248	4/7/2000	Ibiden Co., Ltd.		X
		JP-2000-286554	10/13/2000	Yamaichi Electronics Co., Ltd.		X
		TW-444529	9/17/1999	NEC Corp.		Abstract (equiv.)

NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No.	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
IBP		International Search Report mailed 15 April 2003 for No. PCT/JP02/13434	
IBP		Hiroshi Ohira, et al., "Suggestion of Printed Wiring Board by New Manufacturing Method", A Collection of Papers at the Ninth Lecture Meeting Regarding Circuit Mount, ISSN 0916-0043, 15A-10, pp. 55-56, (1995.3.14-16) (considered title and figure only)	
IBP		Takahiro Mori, et al., "Application and Miniaturization of Substrate Using Inter-Layer Connection Technique Using Bump", A Collection of Papers at the Tenth Lecture Meeting Regarding Circuit Mount ISSN 0916-0043, 15A-09, pp. 79-80, (1996.3.13-15) (considered title and figure only)	
Examiner Signature	/Ishwar Patel/		Date Considered 02/23/2007

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Substitute for form 1449/PTO		INFORMATION DISCLOSURE STATEMENT BY APPLICANT		Complete If Known	
				Application Number	10/500,119
		Filing Date	6/17/2005		
		First Named Inventor	NAKAMURA, et al.		
		Group Art Unit	2841		
		Examiner Name	I. Patel		
		Attorney Docket Number	43988X00		
Sheet	1	of	1		

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IBP		Office Action mailed October 17, 2006 (decision for rejection)	T

Examiner Signature	/Ishwar Patel/	Date Considered	02/23/2007
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